

Form PTO-1449 (modified)

Atty. Docket No.

SILA:073

Serial No.

09/821,340

List of Patents and Publications for Applicant's

Applicants

G. DIWAKAR VISHAKHADATTA ET AL.

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

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Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A1	5,828,955	10/27/98	Lipowski et al.			8/30/95
	A2	6,035,186	3/7/00	Moore et al.			3/11/97
	A3	6,075,979	6/13/00	Holtvoeth et al.			3/5/97
	A4	5,764,171	6/9/98	Stikvoort			4/2/96

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	B1	WO 00/22735	4/20/00	Ali			
	B2	GB2233518A	1/9/91	Dedie			
	B3	0643477A2	3/15/95	Hulkko et al.			
	B4	WO 00/11794	3/2/00	Moore et al.			
	B5	WO 00/01074	1/6/00	Van Der Zwan et al.			

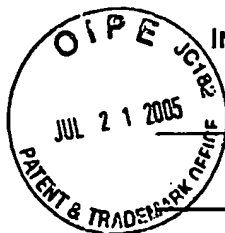
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	C1	Stephen Jantzi et al., "Quadrature Bandpass $\Delta\Sigma$ Modulation for Digital Radio," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 1935-1950.
	C2	Stephen Jantzi et al., "A Complex Bandpass $\Delta\Sigma$ Converter For Digital Radio," ISCAS, May/June 1994, pp. 453-456.
	C3	"Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," Analog Devices Corporate Information Press Release, http://contentanalog.com/pressrelease/prdisplay/0,1622,102,00.html , September 13, 1999, pp. 1-4.

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	C4	Data Sheet, CX74017, "RF Transceiver for Single, Dual, or Tri-Band GSM/GPRS Applications," Conexant, January 2, 2001, pp. 1-16.
↑	C5	Jacques C. Rudell et al., "A 1.9-GHz Wide-Band IF Double Conversion CMOS Receiver for Cordless Telephone Applications," IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 2071-2088.
	C6	Jan Crols et al., "Low-IF Topologies for High-Performance Analog Front Ends of Fully Integrated Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pp. 269-282.
	C7	Jacques C. Rudell et al., "Recent Developments In High Integration Multi-Standard CMOS Transceiver for Personal Communication Systems," invited paper at the 1998 International Symposium on Low Power Electronics, Monterey, California, 6 pgs.
	C8	Asad Abidi, "CMOS Wireless Transceivers: The New Wave," IEEE Communications Magazine, August 1999, pp. 119-124.
	C9	Data Sheet, UAA3535HL, "Low Power GSM/DCS/PCS Multi-band Transceiver," Philips Semiconductors, February 17, 2000, pp. 1-24.
	C10	Stephen Jantzi et al., "FP 13.5: A Quadrature Bandpass $\Delta\Sigma$ Modulator for Digital Radio," Digest of Technical Papers, 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, pp. 216-217, 460.
	C11	S. A. Jantzi et al., "The Effects of Mismatch In Complex Bandpass $\Delta\Sigma$ Modulators," IEEE, 1996, pp. 227-230.
	C12	Qiuting Huang, "CMOS RF Design-The Low Power Dimension," IEEE 2000 Custom Integrated Circuits Conference, pp. 161-166.
	C13	Paolo Orsatti et al., "A 20-mA-Receive, 55-mA-Transmit, Single-Chip GSM Transceiver in 0.25- μ m CMOS," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999, pp. 1869-1880.
✓	C14	Qiuting Huang et al., "The Impact of Scaling Down to Deep Submicron on CMOS RF Circuits," IEEE Journal of Solid-State Circuits, Vol. 33, No. 7, July 1998, pp. 1023-1036.
	C15	Behzad Razavi, "Design Considerations for Direct-Conversion Receivers," IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 44, No. 6, June 1997, pp. 428-435.

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	C16	Farbod Behbahani et al., "CMOS Mixers and Polyphase Filters for Large Image Rejection," IEEE Journal of Solid-State Circuits, Vol. 36, No. 6, June 2001, pp. 873-887.
	C17	Jan Crols et al., "A Single-Chip 900 MHz CMOS Receiver Front-End With A High Performance Low-IF Topolgy," IEEE Journal of Solid-State Circuits, Vol. 30, No. 12, December 1995, pp. 1483-1492.
	C18	Analog Devices, Single-Chip Direct-Conversion GSM/GPRS/EDGE RFIC, Othello One, www.analog.com , 2 pgs.
	C19	Analog Devices, AD6523/AD6524, GSM Direct Conversion Radio Chip Set, www.analog.com , 2 pgs.
	C20	Analog Devices, GSM 3-V Transceiver IF Subsystem, AD6432, www.analog.com , pp. 1-20.

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	A6	4,713,563	12/15/87	Marshall et al.			5/12/86
	A7	4,070,632	1/24/78	Tuttle			9/22/76
	A8	4,236,252	11/25/80	Kominami et al.			2/6/79
	A9	4,680,588	7/14/87	Cantwell			12/5/85
	A10	4,857,928	8/15/89	Gailus et al.			1/28/88
	A11	4,989,074	1/29/91	Matsumoto			9/21/89
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	A24	5,557,642	9/17/96	Williams			11/14/94
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	A30	5,740,524	4/14/98	Pace et al.			12/14/95
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	A33	5,241,310	8/31/93	Tiemann			3/2/92
	A34	4,562,591	12/31/85	Stikvoort			2/2/84
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	A41	4,797,845	1/10/89	Stikvoort			12/11/86
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	A43	5,157,343	10/20/92	Voorman			5/31/91
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↓	A47	6,323,735	11/27/01	Welland et al.			5/25/00
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↑	C22	Analog Devices, AD7002 Specification, LC2MOS, GSM Baseband I/O Port, Rev. B, 1997, pp. 1-16.
	C23	Analog Devices, AD20msp415, GSM/DCS1800/PCS1900, Baseband Processing Chipset, Rev. O, 1997, pp. 1-7.
	C24	Kwentus et al., "A Single-Chip Universal Digital Satellite Receiver With 480-MHz IF Input," IEEE Journal of Solid-State Circuits, Vol. 34, No. 11, November 1999, pp. 1634-1646.
	C25	Minnis et al., "A Low-If Polyphase Receiver For GSM Using Log-Domain Signal Processing," IEEE Radio Frequency Integrated Circuits Symposium, 2000, pp. 83-86.
	C26	Atkinson et al., "A Novel Approach To Direct Conversion RF Receivers For TDMA Applications," Analog Devices, 1999, pp. 1-5.
	C27	Crochiere et al., "Optimum FIR Digital Filter Implementations For Decimation, Interpolation, And Narrow-Band Filtering," IEEE Transactions On Acoustics, Speech, And Signal Processing, Vol. ASSP-23, No. 5, October 1975, pp. 444-456.
	C28	Hogenauer, "An Economical Class Of Digital Filters For Decimation And Interpolation," IEEE, 1981, pp. 155-162.
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	C31	D'Avella et al., "An Adaptive MLSE Receiver For TDMA Digital Mobile Radio," IEEE Journal On Selected Areas In Communications, Vol. 7, No.1, January 1989, pp. 122-129.

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	C34	Lucent Technologies, "DSP1620 Digital Signal Processor," Data Sheet, June 1998, pp. 1-178.
	C35	Steyaert et al., "A 2-V CMOS Cellular Transceiver Front-End," IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, December 2000, pp. 1895-1907.
	C36	Paulus et al., "A CMOS IF Transceiver With Reduced Analog Complexity," IEEE Journal Of Solid-State Circuits, Vol. 33, No. 12, December 1998, pp. 2154-2159.
	C37	Analog Devices, "Analog Devices Delivers World's First Open Market GSM Direct Conversion Radio Chipset," November 1999, 4 pgs.
	C38	"Digest Of Technical Papers," 1997 IEEE International Solid-State Circuits Conference, First Edition, February 1997, 5 pgs.
	C39	RF Micro Devices, RF2968, Product Description, Blue Tooth Transceiver, Rev A19, pp. 11-199-11-222.
	C40	Texas Instruments, TRF6901, "Single Chip RF Transceiver," March 2002, pp. 1-29.
	C41	Texas Instruments, TRF6900A, "Single Chip RF Transceiver," September 2001, pp. 1-34.
	C42	Texas Instruments, TRF6900, "Single Chip RF Transceiver, October 1999, pp. 1-32.
	C43	Philips Semiconductor, "Bluetooth RF Transceiver," Data Sheet, UAA3558, December 21, 2000, pp. 1-5.
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A	C48	Texas Instruments, "TCS2100 GPRS Chipset Solution," Product Bulletin, 2001, 4 pgs.
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	C54	Ericsson, "RF Transceiver Circuit For The Digital Enhanced Cordless Telecommunications (DECT) System," PBL40215, January 2001, pp. 1-22.
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	C62	Shoaei et al., "Optimal (Bandpass) Continuous-Time $\Delta\Sigma$ Modulator," pp. 489-492.
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	C79	Copending U.S. Patent Application Serial No. 10/081,121, filed February 22, 2002, "Calibrated Low-Noise Current And Voltage References And Associated Methods" (Sila:095)
	C80	Copending U.S. Patent Application Serial No. 10/074,591, filed February 13, 2002, "Apparatus For Generating Multiple Radio Frequencies In Communication Circuitry And Associated Methods" (Sila:096)
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	C88	Search Report for PCT/US02/00896; October 4, 2002; 7 pgs.
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Examiner:

Date Considered:

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List of Patents and Publications for Applicant's

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Atty. Docket No.
SILA:073Serial No.
09/821,340Applicants
G. VISHAKHADATTA ET AL.Filing Date:
3/29/01Group:
2631U.S. Patent Documents
See Pages 1-2Foreign Patent Documents
See PageOther Art
See Pages 3-8**Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)**

Exam. Init.	Ref. Des.	Citation
	C90	Copending U.S. Patent Application Serial No. 09/999,702, filed October 31, 2001, "Method And Apparatus For Synthesizing Dual Band High-Frequency Signals For Wireless Communications" (Sila:060C1)

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See Page 1

U.S. Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.

Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.

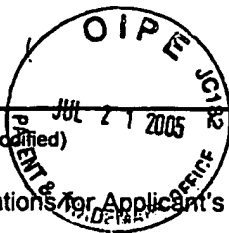
Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

Exam. Init.	Ref. Des.	Citation
	C91	Search Report for PCT/US02/00895, November 7, 2002, 6 pgs.

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Applicants

G. VISHAKHADATTA ET AL.

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2631

U.S. Patent Documents

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Foreign Patent Documents

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Other Art

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U.S. Patent Documents

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Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	B6	WO 99/22456	5/6/99	Grenabo			10/27/98

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

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3/29/01Group:
2631U.S. Patent Documents
See Page 1Foreign Patent Documents
See PageOther Art
See Page**U.S. Patent Documents**

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.
	A49	6,539,066	3/25/03	Heinen			11/10/99
	A50	6,343,207	1/29/02	Hessel et al.			11/3/98
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Foreign Patent Documents

Exam. Init.	Ref. Des.	Document Number	Date	Name	Class	Sub Class	Filing Date if App.

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

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